

CSC 216 Logic and Switching Theory  
Group Homework #4

40pts. 1. Write the following number in 8 bit sign magnitude, one's complement, and 2's complement form.  
SM 1's comp 2's comp

-123

+87

-49

-63

60 pts 10. Convert the following to 8 bit 2's complement numbers. Perform the operation. Identify if an overflow occurred or not. Convert the answer back to decimal.

$$\begin{array}{r} 120 \\ + \underline{67} \end{array}$$

$$\begin{array}{r} -89 \\ - \underline{105} \end{array}$$

$$\begin{array}{r} -72 \\ - \underline{15} \end{array}$$